

**BIPOLAR DIFFERENTIAL INPUT STAGE WITH INPUT BIAS CURRENT
CANCELLATION CIRCUIT**

ABSTRACT OF THE DISCLOSURE

A bipolar differential input stage with an input bias current cancellation circuit comprises an input pair and a bipolar tracking transistor. The input stage is arranged
5 such that the collector currents in the input pair and tracking transistor, and the collector-emitter voltages of the input pair and tracking transistor, are substantially equal. A lateral PNP transistor's first collector provides the tracking transistor base current required to achieve
10 the substantially equal collector current, and second and third collectors provide copies of the tracking transistor base current as bias current cancellation currents to the bases of the input pair, thereby reducing the input stages' input bias currents.